

J1002 U.S. PRO  
 09/824992  
 847082/01

ISSUE CLASSIFICATION	
Class	Subclass

# N

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>07/14</u> Q.A. <u>for</u></p>	<p>PATENT DATE</p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/824992		711	725	2185	Chen, Eddie

## APPLICANTS

OF DIVISION.  
Kevin McGeagh

712

325

~~2185~~

**EXAMINER**

~~Chinitale~~

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4044:Processor which overrides default operand size for implicit stack
4045:  register references and near branches

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PTO-2040  
12/98

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____		<b>ISSUE FEE</b>  Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			<b>ISSUE BATCH NUMBER</b>  _____	
_____ (Primary Examiner)		_____ (Date)		
_____ (Legal Instruments Examiner)		_____ (Date)		

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